Resume
SUID: 4487807

Name	Ankur S Jain		
Address	704 Campus Dr #8C, Stanford CA 94305	**Tel**	650 497 6801
Email	ankur@cs.stanford.edu		
Resume URL	http://www-cs-students.stanford.edu/~ankur/resume.ps (detailed)		

Academic Status Masters: Stanford University (Sep 96 - Mar 98), Comp Sc, GPA: 4.06
Bachelors: IIT Delhi (July 91 - June 95), Comp Sc & Engg, GPA: 9.43 / 10

Areas of Interest Networking & Distributed Systems Operating Systems

Employment Details

Company AT&T Bell Labs (From Sep 1, 95 to Sep 20, 96)
Designation Technical Staff Member (in Networking Research Dept.)
Center Networking and Distributed Systems Research
Nature of work Development, enhancing and porting of a native ATM stack
Achieving end-system quality of service in networks

Company Visigenic Software (July 7, 97 to Sep 23, 97; Summer Internship)
Nature of work Development of Security module for CORBA.

Major Projects

- Signaling Issues in ATM Networks - Handling of link/node failures on an ATM connection, which involved tearing down of VCs and rerouting them along a different path (At IITD)
- Porting of Native ATM Stack - The task was to port Idlinet a Native ATM stack from Brazil (off-shoot of Plan9 developed at Bell Labs) to FreeBSD and Linux kernels (At AT&T Bell Labs)
- Device drivers for ATM cards -Writing device drivers for FORE PCA-200E PCI and Zeitnet 1225 PCI cards so that the ATM stack could operate on different hardware platforms (AT&T Bell Labs)
- Achieving QoS in End-Systems - Changing the packet scheduling and resource allocation at the end system so that the applications reserving bandwidth in the network get enough CPU share at the end-host to be able to process the data (At AT&T Bell Labs)
- VLSI Chip Design - Design of a *self-timed* RSA encoder/decoder. The aim was to design a modest sized CMOS chip. Dual-rail domino logic was used for self-timing purposes, clocks being derived using completion status of previous and next stage. (At Stanford-EE272A/B, Winter ‘97)
- CORBA Security - Development of the security module (authentication and ACL) for CORBA, based on CORBA2.0 security specs (At Visigenic Software - summer '97)
- Connectivity Management for Mobile IP - The objective is to be able to automatically determine the network location while on the move. (At Stanford - Fall 97/Winter 98, in progress)

Academic Honors

- Won the first prize in the novice category in the Student VLSI Design Contest 1997 at the University of Utah (for the RSA chip designed in EE272a)
- Awarded the merit prize thrice in IIT Delhi for obtaining the highest semester GPA
- Ranked 3rd in State Level NTS Exam from a pool of 30,000 students.
- Awarded the National Talent Search Scholarship in 1989.

Programming Languages Proficient in C++, C
Familiar with Java

Other Skills Kernel Programming in FreeBSD and Linux

Coursework

Masters

Introduction to Comp Networks(TA'd)	AI: Principles & Techniques	Introduction to VLSI Systems
Advanced Topics in OS	Deductive Database Theory (CS345)	Design Projects in VLSI
Distributed Systems	Introduction to Computer Graphics	VLSI Systems:Testing & Simulation
Programming Languages	Introduction To Compilers (TA'd)	Financial Decisions
Multimedia Networking & Comm	Introduction to Databases (TA'd)	Business Mgmt for CS professionals

Bachelors (CS courses)

Compilers	Signals & Systems	Digital Integrated Electronics
Systems Programming	Data Structures	Computer Architecture
Data Communications	Discrete Structures	Digital Hardware Design
Introduction to Operating Systems	Theory of Computation	Microprocessors
Introduction to Databases	Analysis & Design of Algorithms	Analog Interface Design
Concurrency Control in Databases	Digital Image Analysis	Advanced Computer Architecture
Computer Networks	Computational Numerical Analysis	CAD of Digital Systems